

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1965	(driv\$4 adj transistor\$2) with (resistance resistive)	US-PGPUB; USPAT	OR	ON	2006/10/02 18:33
L2	2051	(switching adj transistor) with (resistance resistive)	US-PGPUB; USPAT	OR	ON	2006/10/02 18:34
L3	2051	(switching adj transistor\$2) with (resistance resistive)	US-PGPUB; USPAT	OR	ON	2006/10/02 18:18
L4	89	1 and 2	US-PGPUB; USPAT	OR	ON	2006/10/02 18:24
L5	11239	( transistor\$2) with((high\$3 low\$3)near (resistance))	US-PGPUB; USPAT	OR	ON	2006/10/02 18:25
L6	14860	pixel adj (two\$1dimension\$4 array two adj dimension\$4)	US-PGPUB; USPAT	OR	ON	2006/10/02 18:25
L7	94	5 and 6	US-PGPUB; USPAT	OR	ON	2006/10/02 18:26
L8	4140	257/72 257/59 349/43.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/02 18:36
L9	155	5 and 8	US-PGPUB; USPAT	OR	ON	2006/10/02 18:32
L10	115	(transistor\$2) with((intrinsic un\$1dop\$4))and 6	US-PGPUB; USPAT	OR	ON	2006/10/02 18:35
L11	449	(driv\$4 adj transistor\$2) with (resistance resistive)	EPO; JPO; DERWENT	OR	ON	2006/10/02 18:33
L12	856	(switching adj transistor) with (resistance resistive)	EPO; JPO; DERWENT	OR	ON	2006/10/02 18:34
L13	12	11 and 12	EPO; JPO; DERWENT	OR	ON	2006/10/02 18:34
L14	167	(transistor\$2) with((intrinsic un\$1dop\$4))and 8	US-PGPUB; USPAT	OR	ON	2006/10/02 18:36
L15	158	14 not 9	US-PGPUB; USPAT	OR	ON	2006/10/02 18:35
L16	399	257/72 257/59 349/43.ccls.	EPO; JPO	OR	ON	2006/10/02 18:36
L17	0	(transistor\$2) with((intrinsic un\$1dop\$4))and 16	EPO; JPO; DERWENT	OR	ON	2006/10/02 18:37
L18	7	(transistor\$2) with(resistance)and 16	EPO; JPO; DERWENT	OR	ON	2006/10/02 18:37